

AMENDMENTS TO THE CLAIMS

Please replace the claims, including all prior versions, with the listing of claims below.

Listing of Claims:

1. (Currently Amended) An arrangement-(1) for connecting an optical waveguide-(2) to a microprocessor-controlled electrical appliance-(3) having, comprising:
 - [[-]] an arithmetic module (4) which is connected to the electrical appliance (3) and has network functionalities for linking the electrical appliance (3) to a network,
 - [[-]] an interface chip-(7), connected to the arithmetic module-(4), in the form of an integrated circuit chip_{7i} and
 - [[-]] an optical transmission and reception chip-(9) which is connected ~~firstly~~ to the interface chip (7) and ~~secondly~~ to the optical waveguide-(2),

~~characterized in that~~ wherein

 - [[-]] the interface chip (7) has integrated functional modules-(7a, 7b, 7e) which provide at least some of the network functionalities.

2. (Currently Amended) The arrangement-(1) as claimed in claim 1,

~~characterized in that~~ wherein

 - [[-]] at least one functional module-(7a) integrated in the interface chip (7) provides a switch functionality.

3. (Currently Amended) The arrangement (1) as claimed in claim 1 ~~or 2~~,

~~characterized in that~~ wherein

 - [[-]] at least one functional module (7b) integrated in the interface chip-(7) provides a filter functionality.

4. (Currently Amended) The arrangement-(1) as claimed in ~~one of the preceding~~ claims,

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~~characterized in that~~ claim 1, wherein

[[-]] the interface chip ~~(7)~~ is an EPLD.

5. (Currently Amended) The arrangement ~~(1)~~ as claimed in ~~one of claims 1 to 3,~~
~~characterized in that~~ claim 1, wherein

[[-]] the interface chip ~~(7)~~ is an FPGA.

6. (Currently Amended) The arrangement ~~(1)~~ as claimed in ~~one of claims 1 to 3,~~
~~characterized in that~~ claim 1, wherein

[[-]] the interface chip ~~(7)~~ is an ASIC.